
Björn Berglöv

Tallsåtravägen 8
144 63 Rönninge
SWEDEN

+ 46-8-532 532 60 (home)
bjorn@mrbear.se
www.mrbear.se

Education

Master of Science in Electrical Engineering
Royal Institute of Technology, Stockholm 1986.
Technician degree 1978.

Areas of knowledge

ASIC, FPGA design	Telecom
Microprocessors	Power switching
Batteries, Accumulators	Video processing
Analog electronics	Teaching

Experience

- 11 - present** **Independent Consultant.**
Contract with Xelerated, Stockholm.
Asic prototyping in top-end Xilinx FPGAs, means that hardware and software can be verified together before the asic is manufactured.
- 06 - 10** **Senior HW Designer at Edgware**
Designing a **20Gb/s Video-on-demand** server. Several Altera FPGA designs including AES and MD5 crypto, packet classification, metering/shaping of traffic as well as various high-speed interfaces and BIST's.
- 02 - 06** **Senior HW Designer at Xelerated**
Designing **40Gbit/s network processor** (100Mgate+) Responsible for generic DDR-SRAM/FCRAM/RLDRAM/NSE programmable interfaces, handling i/o at 530Mbps/pin. Also designed co-processor in Xilinx FPGA handling high performance shaping and metering.
- 01 - 02** **Independent Consultant.**
Contract with Ericsson Telecom in Älvsjö, Stockholm.
Designing a voice processing ASIC for the AXD301 high performance **ATM switch**. Designing high performance arithmetic blocks. Used Celaro hardware acclerator for verification.
- 97 - 01** **Independent Consultant.**
Contract with Ericsson Saab Avionics, in Kista, Stockholm.
Designing a graphics processor ASIC for a **fighter jet plane**. Tool support and contacts with Vendor. Several Altera CPLD/FPGA designs for video processing as well as C++ code in embedded VxWorks environment. Researching the use of commercial **graphics processors** in mil spec environments. As senior designer supervising several projects as well as **giving courses and lectures**.
- 98 - 00** **Part-time Advicer, lecturer.**
Some shorter engagements with ContextVision in Linköping (design house and vendor selection, contract review) and C technologies in Lund (advising design flow, course in various front-/back-end issues).

- 96 - 97 Subcontractor to Mentor Graphics.**
Contract with Ericsson Communication Systems, Lund. Asics for **GSM and AMPS mobile phones**. Low voltage and low power design, using VLSI 0.5u and 0.35u asic technology (400+ kgates). Responsible for deep-submicron design flow, and handled libraries and contacts with vendor. Synopsys tool support. Using VHDL and several Mentor Graphics tools.
- 93 - 95 Independent Consultant.**
Contract with Ericsson Telecom, Stockholm.
Designing SDH telecom equipment using Motorola 0.5u asic technology (300+ kgates), in Verilog and Synopsys environment. Several designs including: JTAG logic, High Speed block (300 MHz). Responsible for generating functional test vectors and ATPG vectors.
- 91 - 93 Consulting ASIC designer. ÅF-Industriteknik, Stockholm.**
Design of four **Texas Instruments** ASICs in **0.7u BiCMOS** process (up to 70kgates) handling up to **184 Mbit/s** data rate for Ericsson **SDH** equipment. Using **Verilog** and **Synopsys** on **SUN** workstations in **UNIX** environment. Responsible for bus interface to Motorola **68302**, **68360**. Wrote syntax checking tool in C. Also designed in **Motorola H4C** technology. Using **Motive** static timing analyzer.
- 88 - 91 Consulting engineer. ÅF-Industriteknik, Stockholm.**
Designing **modems** for laptop computers. The work included a lot of traveling, troubleshooting and contacts with the telecom authorities in several countries. Analog/digital design and real-time assembly programming INTEL 8096. Several designs intended for outdoor environment including power supplies, battery operated equipment and battery chargers. Participated in a demanding 6 month manager training program.
- 86 - 88 Lecturer at The University of Zambia (Africa).**
Giving course: 'Communication Theory and Systems' for final year telecom. students. Supervising BA students electronics projects.
- 81 - 86 Master of Science student. Royal institute of Technology, Stockholm, Sweden.**
Master's project 'Fiber-Optic Power supply' lead to file a **Patent application**. During studies, part time teacher in various undergraduate courses.
- 79 - 81 Test engineer at Ellemtel, Stockholm.**
Writing programs at HP test equipment and computers used for production testing and measurement of telecom equipment.
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Personal

Outgoing, creative and responsible.
Travel / relocate subject needs.
Fluent in English, some German.
Thirty years as scuba diver, one star Instructor level.
Married.
Born Dec 16, 1958.
Enjoying excellent health.

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