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# Björn Berglöf

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## Education

**Master of Science in Electrical Engineering** Royal Institute of Technology, Stockholm, Sweden. May 1986.  
**Technician degree** May 1978.

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## Areas of knowledge

ASIC, FPGA design	Telecommunication
Microprocessors	Modems
Assembler	Power switching
Batteries, Accumulators	Graphics processors
Analog electronics	Video processing
Teaching	Micro-power electronics

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## Experience

- 6/02 - current** **Designer** in an ASIC team at Xelerated, Stockholm.  
Designing high performance 40Gbit/s network processors.
- 4/01 - 5/02** **Independent Consultant.**  
Contract with Ericsson Telecom in Älvsjö, Stockholm.  
Designing a voice processing ASIC for the AXD301 high performance ATM switch.  
Designing high performance arithmetic blocks. Celero hardware accelerator for verification.
- 7/97 - 3/01** **Independent Consultant.**  
Contract with Ericsson Saab Avionics, in Kista, Stockholm.  
Designing a graphics processor ASIC for a **fighter jet plane**. Tool support and contacts with Vendor. Several Altera CPLD/FPGA designs for video processing as well as C++ code in embedded VxWorks environment. Researching the use of commercial **graphics processors** in mil spec environments. As senior designer supervising several projects as well as **giving courses and lectures**.
- 3/98 - 9/00** **Adviser, lecturer.**  
Some shorter engagements with ContextVision in Linköping (design house and vendor selection, contract review) and C technologies in Lund (advising design flow, course in various front-/back-end issues).
- 2/96 - 6/97** **Subcontractor to Mentor Graphics.**  
Contract with Ericsson Communication Systems, Lund.  
Designing with the **VLSI 0.5u** and **0.35u** asic technology (400+ kgates). Responsible for deep-submicron design flow. Synopsys tool support. Using **VHDL** and several **Mentor Graphics** tools. Also handled libraries and contacts with vendor. Asics for **GSM and AMPS mobile phones**. Low voltage and low power design.
- 1/93 - 6/95** **Independent Consultant.**  
Contract with Ericsson Telecom, Stockholm.

Designing with the Motorola **0.5u** asic technology (300+ kgates). Using **Verilog** and **Synopsys**. Several designs including: **JTAG** logic, High Speed block (300 MHz). Responsible for generating functional test vectors and **ATPG** vectors using **Hitest** and **Synopsys Test Compiler**.

**6/91 - 1/93 Consulting ASIC designer. ÅF-Industri teknik, Stockholm.**

Design of four **Texas Instruments** ASICs in **0.7u BiCMOS** process (up to 70kgates) handling up to **184 Mbit/s** data rate for Ericsson **SDH** equipment. Using **Verilog** and **Synopsys** on **SUN** workstations in **UNIX** environment. Responsible for bus interface to Motorola **68302**, **68360**. Wrote syntax checking tool in C. Also designed in **Motorola H4C** technology. Using **Motive** static timing analyzer.

**4/88 - 6/91 Consulting engineer. ÅF-Industri teknik, Stockholm.**

Designing **modems** for laptop computers. The work included a lot of traveling, troubleshooting and contacts with the telecom authorities in several countries. Analog/digital design and real-time assembly programming **INTEL 8096**. Several designs intended for outdoor environment including power supplies, battery operated equipment and battery chargers. Participated in a demanding 6 month manager training program.

**8/86 - 3/88 Lecturer at The University of Zambia (Africa).**

Giving course: 'Communication Theory and Systems' for last year telecom. students. Supervising BA students electronics projects.

**9/81 - 8/86 MS student. Royal institute of Technology, Stockholm, Sweden.**

Master's project 'Fiber-Optic Power supply' lead to file a **Patent application**. During studies, part time teacher in various undergraduate courses.

**6/79 - 9/81 Programmer at Ellementel, Stockholm.**

Writing programs at HP test equipment and computers used for production testing and measurement of telecom equipment.

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## Personal

Outgoing, creative and responsible.  
Travel / relocate subject needs.  
Fluent in English, some German.  
Twenty years as scuba diver, one star Instructor level.  
Married.  
Born Dec 16, 1958.  
Enjoying excellent health.

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